Course

Design Project B

COMP4601 | 6 Units of Credit

Overview

Hardware design and systems prototyping using field-programmable gate arrays. Emphasises laboratory experience in programming and interfacing with FPGAs. Acceleration of soft-core processors, design of custom accelerator blocks and configurable logic-based applications from areas such as telecommunications, signal processing, cryptography and biological sequencing. Techniques for designing and implementing configurable systems. CAD challenges posed by the area. Design validation, performance analysis and power consumption. Reconfigurable systems.
**Faculty**
Faculty of Engineering

**School**
School of Computer Science and Engineering

**Study Level**
Undergraduate

**Offering Terms**
Term 1

**Campus**
Kensington

**Indicative contact hours**
6

**Timetable**
Visit timetable website for details
Conditions for Enrolment

Prerequisite: COMP3211 and COMP3601.
Course Outline

To access course outline, please visit:

COMP4601 Course Outline
**Fees**

- Commonwealth Supported Students: $1191
- Domestic Students: $5970
- International Students: $5970

**DISCLAIMER**

Please note that the University reserves the right to vary student fees in line with relevant legislation. This fee information is provided as a guide and more specific information about fees, including fee policy, can be found on the [fee website](#).

For advice about fees for courses with a fee displayed as "Not Applicable", including some Work Experience and UNSW Canberra at ADFA courses, please contact the relevant Faculty.

Where a Commonwealth Supported Students fee is displayed, it does not guarantee such places are available.
Pre-2019 Handbook Editions

Access past handbook editions (2018 and prior)

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